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### (54) Residue removal by supercritical fluids

(57) A method for the removal of residue from an etched precision surface such as a semiconductor sample is provided which comprises exposing said precision surface to a supercritical fluid or liquid CO<sub>2</sub> under appropriate conditions that are sufficient to remove the residue from the precision surface. Cryogenic aerosol may be used in conjunction with either the supercritical fluid or liquid CO<sub>2</sub>.

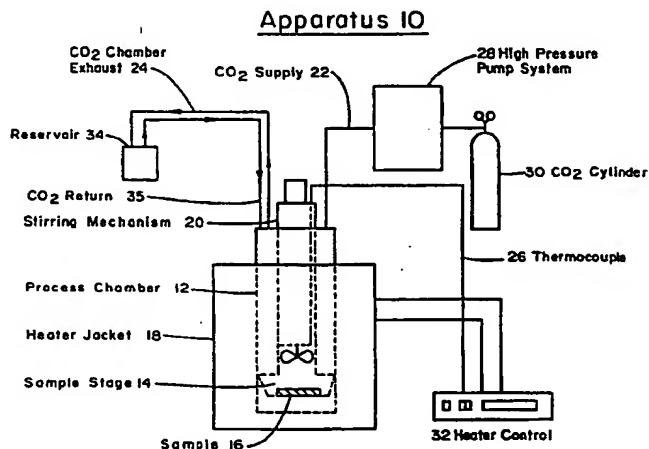


FIG.1

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## Description

The present invention relates to a method for removing residue material from a precision surface, e.g. a semiconductor sample, which has been first subjected to an etching process, such as reactive ion etching, (RIE).

In the field of advanced semiconductor manufacturing, it is well known to expose a semiconductor sample, such as a semiconductor wafer containing blanket metal or insulating films and a photoresist exposed with patterns to a reactive ion etching (RIE) process using a mixture of gases containing but not limited to chlorine and/or fluorine. The purpose of such an etching process is to define patterns in the films. The photoresist is then typically stripped in an oxygen plasma. The remaining residues often need to be removed by chemicals and/or solvents in order to achieve high yield.

For example, Al metal etching is the most commonly used to define wiring on the semiconductor wafers. Despite cleaning and rinsing the semiconductor wafer, unwanted residue still remains on the top and the sidewalls of the metal lines. This unwanted residue, which remains on the top and sidewalls of the metal lines, reportedly includes the elements carbon, hydrogen, silicon, aluminum, fluorine, chlorine and oxygen. Such residue, which is referred to herein as RIE residue, is known to be conductive enough to cause shorts between metal lines. Moreover, the RIE residue may also cause adhesion problems between the metal lines and the overlying insulator. The RIE residue on metal lines may cause corrosion of the semiconductor sample. The RIE residue on polysilicon lines or oxide vias also cause yield loss problems. Thus, there is considerable interest in the field of advanced semiconductor manufacturing for developing a chemically safe and easy method for removing the RIE residue from a semiconductor sample.

The current method which is typically being used for removal of this unwanted RIE residue in advanced semiconductor manufacturing processes involves soaking the etched semiconductor sample in an acid bath.

The present invention provides a method for removing residue from an etched precision surface comprising exposing said precision surface to a removing fluid comprising supercritical fluid or liquid CO<sub>2</sub> under conditions sufficient to remove said residue from said etch precision surface.

Preferably, the precision surface is a semiconductor sample, which has been first subjected to an etching process such as reactive ion etching (RIE).

An optional two step process for removing residue from an etched precision surface is provided, using cryogenic aerosol to remove residue remaining after the supercritical fluid or liquid CO<sub>2</sub> treatment, is also contemplated herein.

The method of the present invention eliminates the use of prior art solvents and acids in selected aspects of

advanced semiconductor manufacturing processes for removing residue from a precision surface, e.g. a semiconductor sample.

It is emphasized that supercritical fluids, such as supercritical fluid CO<sub>2</sub>, are, however, currently being used in semiconductor processing for developing a resist pattern layer on a substrate. Such a process is disclosed, for example, in U.S. Patent No. 4,944,837 to Nishikawa et al. Specifically, Nishikawa et al. provides a method of forming a patterned resist film having a predetermined pattern on a surface layer formed on a substrate comprising the steps of depositing a resist film on the surface layer, pre-processing the resist film into a pre-processed resist film which is attached to the surface layer and which has a latent image of the predetermined pattern, and processing the pre-processed resist film into the patterned resist film. In accordance with the disclosure of Nishikawa et al., the processing step comprises introducing the pre-processed resist film together with the substrate into a supercritical atmosphere and developing the pre-processed film in a supercritical atmosphere to selectively remove the pre-processed film.

Other examples of using supercritical fluids in semiconductor manufacturing are disclosed in U.S. Patent Nos. 5,185,296 and 5,304,515, both to Morita et al. In both of these disclosures, supercritical fluids are used for forming a dielectric thin film or pattern thereof on the surface of a semiconductor substrate. As in the above reference to Nishikawa et al., the supercritical fluids are used in both of the Morita et al. references to develop the pattern resist film on the surface of the semiconductor substrate.

In an article by Ziger et al. entitled "Compressed Fluid Technology: Application to RIE-Developed Resists", AICHE Journal, Vol. 33, No. 10, October 1978, compressed CO<sub>2</sub> i.e., supercritical fluid CO<sub>2</sub>, is utilized in the area of microlithography to extract nonvolatile siloxane molecules from a host organic polymer.

Despite the use of supercritical fluids in the prior art there is no known disclosure of using a supercritical fluid to remove residue from a precision surface such as a semiconductor sample which contains such residue thereon.

It is emphasized that the residue on the precision surface removed by the method of the present invention, is formed by subjecting the surface to a material removal process such as chemical etching, ion etching, or laser ablation, in the process of creating or modifying the precision surface. The residue formed may be on etched surfaces or adjacent non-etched surfaces. The residue is then removed from the surface by exposure to a supercritical fluid.

The use of supercritical fluids for removing residue from precision surfaces such as semiconductor samples eliminates the prior art use of, but not limited to, a carcinogenic bath containing chromic phosphoric acid. Thus, the present invention provides an efficient and

safe means for removing etchant residue from a precision surface containing such residue.

The term "precision surface" as used herein denotes a material which has controlled features below the plane of the surface such as cavities, trenches or channels incorporated into the material and or raised features such as mesas. Cleaning of this type of surface must be selective to the residue and not modify the surface geometry (dimensions). Precision surfaces include, but are not limited to, semiconductor samples, metals, polymers and insulators.

The term "supercritical fluid" is used herein to denote a material which is under conditions of not lower than a critical temperature,  $T_c$ , and not less than a critical pressure,  $P_c$ , in a pressure-temperature diagram of an intended compound. For a complete description on the theory of supercritical fluids see Kirk-Othmer Encyclopedia of Chemical Technology, 3d, Supplement Volume, pp. 872-893. The preferred supercritical fluid employed in the present invention is  $\text{CO}_2$  which may be used alone or in an admixture with another additive such as  $\text{Ar}$ ,  $\text{NH}_3$ ,  $\text{N}_2$ ,  $\text{CH}_4$ ,  $\text{C}_2\text{H}_4$ ,  $\text{CHF}_3$ ,  $\text{C}_2\text{H}_6$ ,  $n\text{-C}_3\text{H}_8$ ,  $\text{H}_2\text{O}$ ,  $\text{N}_2\text{O}$  and the like. Surfactants containing at least one  $\text{CF}_x$  functional group may also be used in conjunction with a supercritical fluid.

Embodiments of the present invention will now be described with reference to the accompanying drawings, in which:

Fig. 1 is a schematic diagram of the apparatus which is employed in the instant invention for the removal of residue from a precision surface such as a semiconductor sample.

Fig. 2 is an SEM illustrating the metal line structures of a 200 mm semiconductor wafer which contains RIE residue, which was broken into several samples for testing.

Fig. 3 is an SEM of a first piece of the wafer of Fig. 2 after exposure to supercritical fluid  $\text{CO}_2$  for 2 hrs. at  $40^\circ\text{C}$  and 5840 psi, 50K magnification, resolution 600 nm.

Fig. 4 is an SEM of the second piece of the wafer of Fig. 2 after exposure to supercritical  $\text{CO}_2$  for 2 hrs. at  $80^\circ\text{C}$  and 5840 psi, 50K magnification, resolution 600 nm.

Fig. 5 is an SEM of the third piece of the wafer of Fig. 2 after exposure to supercritical fluid  $\text{CO}_2$  for 30 minutes at  $40^\circ\text{C}$  and 5840 psi, 35K magnification, resolution 857 nm.

Fig. 6 is an SEM of the fourth piece of the wafer of Fig. 2 after exposure to supercritical fluid  $\text{CO}_2$  for 1 hr. at  $40^\circ\text{C}$  and 5840 psi, 60K magnification, resolution 500 nm.

Fig. 7 is an SEM of the fifth piece of the wafer of Fig. 2 after exposure to supercritical  $\text{CO}_2$  for 2 hr. at  $0^\circ\text{C}$  and 5840 psi, 60K magnification, resolution 500 nm.

Fig. 8 is an SEM of the first piece of a 200 mm semiconductor wafer with vias etched in the oxide film shown from the side, 5KV, 60K magnification, resolution 600 nm.

Fig. 9 is an SEM of a second piece of the 200 mm semiconductor wafer of Figure 8 with vias etched in the oxide film, shown from the side, after processing in supercritical fluids, 10KV, 60K magnification, resolution 500 nm.

In accordance with the first embodiment of the present invention, residue which is present on an etched precision surface is removed from the precision surface by using supercritical fluids. Specifically, the residue is removed from an etched precision surface by exposing the precision surface to a supercritical fluid under conditions which are sufficient to remove said residue.

It is emphasized that the precision surface is first etched prior to exposing it to a supercritical fluid. Etching of the precision surface may be conducted using techniques well known to those skilled in the art. Suitable techniques for etching the precision surface include, but are not limited to, reactive ion etching (RIE), ion beam etching (IBE), plasma etching, laser ablation and the like. Of these etching techniques, RIE is particularly preferred in the present invention. Typically, in the prior art, RIE is carried out using gases containing, but not limited to, Cl or F.

The residue left behind after etching a precision surface may contain one or more of the following elements: carbon, hydrogen, silicon, aluminum, fluorine, chlorine or oxygen.

As stated above, the term "precision surface" is used herein denote a material which contains a surface that has cavities, trenches and/or channels incorporated therein. Suitable precision surfaces that may be employed in the present invention include, but are not limited to, semiconductor samples, metals such as Al, Si, W, Ti, Ta, Pt, Pd, Ir, Cr, Cu, and Ag, polymers such as polyimides, polyamides and the like, and insulators. Of these precision surfaces, semiconductor samples are particularly preferred in the present invention.

It should be noted that the description provided hereinbelow while being specific to RIE semiconductor samples is also valid for other types of precision surfaces which may be etched by any of the aforementioned etching techniques. For example, the description provided hereinbelow also applies to an IBE insulator, a laser ablated polymer and the like.

Figure 1 is a schematic diagram of an apparatus 10 that can be used in the present invention for removing

RIE residue from a semiconductor sample containing the same. Apparatus 10 includes process chamber 12 having a sample zone 14 wherein the semiconductor sample 16 is placed. The process chamber 12 is surrounded by heater jacket 18 and contains, optionally, a stirring mechanism 20. Additionally, the process chamber contains inlet line 22, outduct 24 and thermocouple 26. The inlet line 22 contains a high pressure pump system 28 which is connected to gas cylinder 30 for supplying a supercritical fluid or mixture thereof to process chamber 12. Thermocouple 26 is also connected to heater control 32 which is utilized for controlling and monitoring the temperature of the RIE residue removal process. Apparatus 10 may also include reservoir 34 for collecting and/or purifying supercritical fluids that exit process chamber 12 through outduct 24. This material may then be recycled into the process chamber via duct 35. Gas cylinder 30 contains a pressurized liquid. The term supercritical fluid refers to the state of matter of a material above its critical point, i.e., a critical temperature,  $T_c$ , and critical pressure,  $P_c$ , at which two phases of a substance, in equilibrium with each other, become identical, forming one phase. Any supercritical fluid known to those skilled in the art such as  $\text{CO}_2$  and/or Ar may be used in the present invention provided that they are capable of removing the RIE residue from the semiconductor sample. The preferred supercritical fluid is  $\text{CO}_2$  which may be used alone or in an admixture with one or more additives selective from the group consisting of Ar,  $\text{N}_2\text{O}$ ,  $\text{NH}_3$ ,  $\text{N}_2$ ,  $\text{CH}_4$ ,  $\text{C}_2\text{H}_4$ ,  $\text{CHF}_3$ ,  $\text{C}_2\text{H}_6$ ,  $\text{H}_2\text{O}$ ,  $n\text{-C}_3\text{H}_8$ , and the like.

Any grade of supercritical fluid can be employed in the present invention. If a low grade of supercritical fluid is employed which contains a lot of impurities therein, the supercritical fluid can be first purified to remove the impurities using techniques well known to those skilled in the art. For instance, the low grade supercritical fluid could be purified by passing it through a column prior to entering the processing chamber.

It is also emphasized that the supercritical fluid could be combined with additives or surfactants which would aid in removing the RIE residue from the semiconductor sample. Suitable additives include, but are not limited to, those mentioned hereinabove. Of these additives,  $\text{H}_2\text{O}$  is most particularly preferred.

The types of surfactants that may be used in the present invention include any surfactant which contains at least one  $\text{CF}_x$  functional group in its structure.

As shown in Figure 1, the supercritical fluid is pre-pressurized with a high pressure pump. Typically, in the present invention, the supercritical fluid is pre-pressurized to a pressure of about 1000 psi to 6000 psi. More preferably, the supercritical fluid is pre-pressurized to a pressure of about 3000 psi before entering the processing chamber. The pre-pressurized supercritical fluid is then transferred to the processing chamber which contains a semiconductor sample through inlet line 22.

The semiconductor samples that can be employed

in the present invention are any semiconductor samples that are processed by RIE or any of the other etching techniques mentioned hereinabove. Illustrated examples of suitable semiconductor samples that may be used in the present invention include, but are not limited to, semiconductor wafers, semiconductor chips, ceramic substrates, patterned film structures and the like.

Besides what is used in illustrating the invention, the semiconductor sample, which may be subjected to the method of the present invention, may include one or more of the following materials: titanium silicides, tantalum nitride, tantalum silicide, silicon, polysilicon, silicon nitride,  $\text{SiO}_2$ , diamond-like carbon, polyimides, polyamides, aluminum, aluminum with copper, copper, tungsten, titanium, palladium, platinum, iridium, chromium, ferroelectric materials and high dielectric materials such as  $\text{BaSrTi}$  or  $\text{PbLaTi}$  oxides.

The semiconductor sample containing the RIE residue is placed in sample zone 16 of process chamber 12 wherein the sample is exposed to the supercritical fluid under conditions which are sufficient to remove the RIE residue from the sample while maintaining the supercritical fluid above its critical temperature and pressure.

Typically, in the present invention the pressure within the process chamber during RIE residue removal is from about 1000 psi to about 6000 psi. More preferably, the pressure within the process chamber during RIE residue removal is about 3000 psi.

The temperature within the processing chamber during the RIE residue removal which is monitored by thermocouple 26 and controlled by controller 32 is generally from about  $40^\circ\text{C}$  to about  $80^\circ\text{C}$ . More preferably, the temperature within the process chamber during RIE residue removal is about  $40^\circ\text{C}$ .

To ensure effective removal of the RIE residue from the semiconductor sample, the semiconductor sample should be exposed to the supercritical fluid under the above conditions for about 30 minutes to about 2 hrs. More preferably, the time period for exposure of the semiconductor sample to the supercritical fluid under the above-identified conditions is about 1 hr.

The supercritical fluid exiting the process chamber through outduct 24 may be cleaned, as described above, and recycled back into the apparatus so as to form a closed reactor system. Such a closed reactor system, which is not shown in Fig. 1, would greatly reduce the processing cost in producing clean semiconductor samples.

When stirring is employed within the processing chamber, the speed of the stirring unit may vary from about 500 rpm. to about 2500 rpm., preferably about 1000 rpm.

In accordance with the second embodiment of the present invention, a method for removing residue from an etched precision surface such as a RIE semiconductor sample is provided which comprises the steps of exposing the precision surface to a supercritical fluid

and then contacting the exposed precision surface to a cryogenic aerosol. A highly preferred precision surface used in this embodiment of the present invention is a RIE semiconductor sample.

The term "cryogenic aerosol" as used herein denotes a solid jet spray which is formed when a relatively high pressure gas liquid mixture is allowed to rapidly expand into a region of lower pressure at cryogenic temperatures, cooling the jet and causing the mixture to solidify. Cryogenic aerosols comprising argon, nitrogen and/or CO<sub>2</sub> may be employed in the present invention in removing residue from a precision surface.

When cryogenic aerosol is used, the cryogenic aerosol contacts the exposed semiconductor sample under conditions which are sufficient to remove the remaining residue detached during the supercritical fluid treatment process. Such conditions are well known to those skilled in the art.

In the third embodiment of the present invention, liquid CO<sub>2</sub> is used as the solvent instead of a supercritical fluid for removing residue from a precision surface that has been previously etched by one of the aforementioned etching processes. The preferred precision surface employed in this third embodiment of the instant invention is a semiconductor sample which was been etched by RIE.

The apparatus used in this third embodiment of the present invention is similar to the one shown in Figure 1 except that the gas cylinder contains gaseous CO<sub>2</sub> which is made into a liquid by pre-pressurizing the gas to a total pressure of about 880 psi to about 1000 psi. More preferably, the gaseous CO<sub>2</sub> is pressurized to about 880 psi.

Any grade of gaseous CO<sub>2</sub> made be used, however, if the impurity level within the gas is too high the gas should be purified by the above identified techniques prior to be converted into the liquid state.

The conditions used in this third embodiment are not as severe as that described above since no supercritical fluid is employed. Typically, in this third embodiment, the pressure within the processing chamber during residue removal is from about 880 psi to about 1000 psi. More preferably, when liquid CO<sub>2</sub> is employed the pressure within the processing chamber is about 880 psi.

The temperature which is used in this third embodiment of the present invention is generally from about 25°C to about 40°C. More preferably, when liquid CO<sub>2</sub> is employed, the temperature within the processing chamber during residue removal is about 40°C.

Sufficient residue removal using liquid CO<sub>2</sub> is obtained generally within a period of time of from about 30 minutes to about 2 hrs. More preferably, sufficient residue removal using liquid CO<sub>2</sub> is generally obtained within a time period of from about 1 hr.

The liquid CO<sub>2</sub> may be used alone or it may be used with one of the surfactants or additives described hereinabove. The preferred additive used with liquid

CO<sub>2</sub> is H<sub>2</sub>O.

When stirring is employed in this third embodiment, the stirring speed is from about 500 rpm to about 2500 rpm. More preferably, the stirring speed in the third embodiment is from about 1000 rpm.

In the fourth embodiment of the present invention, the residue is removed from an etched precision surface using a two step process which comprises first exposing a precision surface containing etchant residue to liquid CO<sub>2</sub> under conditions defined above and then contacting the exposed semiconductor sample with a cryogenic aerosol under conditions to remove the remaining residue detached during the liquid CO<sub>2</sub> treatment.

The cryogenic aerosol used in this embodiment of the present invention is the same as that described hereinabove in the second embodiment. Moreover, the conditions are also the same as that reported hereinabove.

The following examples are given to illustrate the scope of the present invention. Because these examples are given for illustrative purposes only, the invention embodied therein should not be limited thereto.

#### EXAMPLE 1

In this example, RIE residue is removed from semiconductor wafers using supercritical fluid CO<sub>2</sub> as the solvent. Specifically, the semiconductor wafers containing a blanket metallization and a patterned photoresist were first exposed to a typical reactive ion etch process.

After etching of aluminum lines, the photoresist was stripped in an O<sub>2</sub>-containing plasma using techniques well known to those skilled in the art.

A Scanning Electron Micrograph (SEM), of one of the wafers after processing is shown in Figure 2. All the SEM's shown in Figs 2-7 of this example were done at 10 KEV using various magnifications and resolutions. Views from the top as well as the sides are also shown. Specifically, Figure 2 shows the metal line structure of the RIE wafer which contains RIE residue deposit thereon.

Next, wafers, as shown in Figure 2, containing RIE residue were loaded into a high pressure chamber as shown in Figure 1. Supercritical extraction grade CO<sub>2</sub> was pre-pressurized to about 5840 psi using a mechanical pump and was introduced into the pressure chamber by using static pressure and flow mode. No stirring was utilized in this example.

In a first experiment, one of the wafers was exposed to supercritical fluid CO<sub>2</sub> for 2 hrs at a temperature of 40°C and a pressure of 5840 psi. An SEM for this experiment is shown in Fig. 3. Specifically, this SEM shows a cleaned semiconductor sample. The sample initially contained RIE residue prior to exposure to a supercritical fluid under the above conditions.

In another experiment, a wafer was exposed to supercritical fluid CO<sub>2</sub> for 2 hrs. at a temperature of 80°C and a pressure of 5840 psi. An SEM for this exper-

iment is shown in Fig. 4. Again, this SEM illustrates that effective RIE residue removal is obtained by using the method of the present invention.

Further experiments were made at 40°C and 5840 psi using processing times of 30 minutes, 1 hour and 2 hours. Those results are shown in Figs. 5, 6 and 7, respectively. The RIE residue removal from the semiconductor wafers was shown to be more effective at 1 or 2 hours than at 30 minutes.

## EXAMPLE 2

In this example, a section of a 200mm wafer with vias etched in the oxide was subjected to supercritical CO<sub>2</sub>.

Residue that was formed in the vias can be seen in Fig. 8 (side view). The wafer was exposed to supercritical fluid CO<sub>2</sub> for 1 hour at 3000 psi and 40°C with stirring at 500 rpm. As shown in Figure Fig. 9 (side view), all RIE residue has been removed from the sample under these conditions.

## Claims

1. A method for removing residue from an etched precision surface comprising exposing said precision surface to a removing fluid comprising supercritical fluid or liquid CO<sub>2</sub> under conditions sufficient to remove said residue from said etch precision surface.
2. The method of claim 1 wherein said removing fluid is a supercritical fluid.
3. The method of Claim 2 wherein said supercritical fluid comprises Ar or CO<sub>2</sub> or mixtures thereof.
4. The method of Claim 2 wherein said surface is exposed to said supercritical fluid at a pressure of from about 1000 psi to about 6000 psi.
5. The method of Claim 4 wherein said pressure is about 3000 psi.
6. The method of Claim 2 wherein said surface is exposed to said supercritical fluid at a temperature of about 40°C to about 80°C.
7. The method of claim 1 wherein said removing fluid is liquid CO<sub>2</sub>.
8. The method of Claim 1, 2 or 7 wherein the precision surface is a semiconductor sample, a metal, a polymer or an insulator.
9. The method of Claims 1 to 8 wherein the precision surface is etched by reactive ion etching, ion beam etching, plasma etching or laser ablation.
10. The method of Claim 9 wherein etching is conducted by reactive ion etching.
11. The method of Claim 7 wherein said precision surface is exposed to said liquid CO<sub>2</sub> at a pressure of about 880 psi to about 1000 psi.
12. The method of Claim 7 wherein said precision surface is exposed to said liquid CO<sub>2</sub> at a temperature of from about 25°C to about 40°C.
13. The method of Claim 1, 2 or 7 wherein said precision surface is exposed to said removing liquid for a time period of about 30 minutes to about 2 hrs.
14. The method of Claim 13 wherein said exposure time is about 1 hr.
15. The method of Claim 8 wherein said semiconductor sample is a surface of a semiconductor wafer, semiconductor chip, ceramic substrate, glass substrate, polymer or other patterned film structure.
16. The method of claim 1, 2 or 7 wherein said precision surface contains a material selected from the group consisting of titanium silicide, tantalum nitride, silicon, polysilicon, silicon nitride, SiO<sub>2</sub>, diamond-like carbon, polyimides, polyamides, aluminum, aluminum with copper, copper, Ti, Ta, W, Pt, Pd, Ir, Cr, ferroelectric materials and high dielectric materials.
17. The method of Claim 1, 2 or 7 wherein said removing fluid is stirred at about 500 rpm to about 2500 rpm.
18. The method of Claim 17 wherein said removing fluid is stirred at about 1000 rpm.
19. The method of Claim 1, 2 or 7 wherein an additive or surfactant is used with said removing fluid.
20. The method of Claim 19 wherein said additive is selected from the group consisting of Ar, N<sub>2</sub>O, NH<sub>3</sub>, N<sub>2</sub>, CH<sub>4</sub>, C<sub>2</sub>H<sub>4</sub>, CHF<sub>3</sub>, C<sub>2</sub>H<sub>6</sub>, n-C<sub>3</sub>H<sub>8</sub> and H<sub>2</sub>O.
21. The method of Claim 19 wherein said surfactant is a surfactant which contains at least one CF<sub>x</sub> functional groups.
22. The method of Claim 1, 2 or 7 wherein said removing fluid is purified prior to exposing to said precision surface.
23. The method of Claim 1, 2 or 7 wherein said residue contains at least one element selected from the group consisting of carbon, hydrogen, silicon, aluminum, W, Ti, Ta, Pt, Pd, Ir, Cr, fluorine and chlo-

rine.

24. The method of Claim 1, 2 or 7 further comprising  
contacting said removing fluid exposed precision  
surface with a cryogenic aerosol under conditions 5  
sufficient to remove any remaining residue from the  
exposed precision surface.
25. The method of Claim 24 wherein said cryogenic  
aerosol of Ar, N<sub>2</sub>, CO<sub>2</sub> or a mixture thereof. 10

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# Apparatus 10

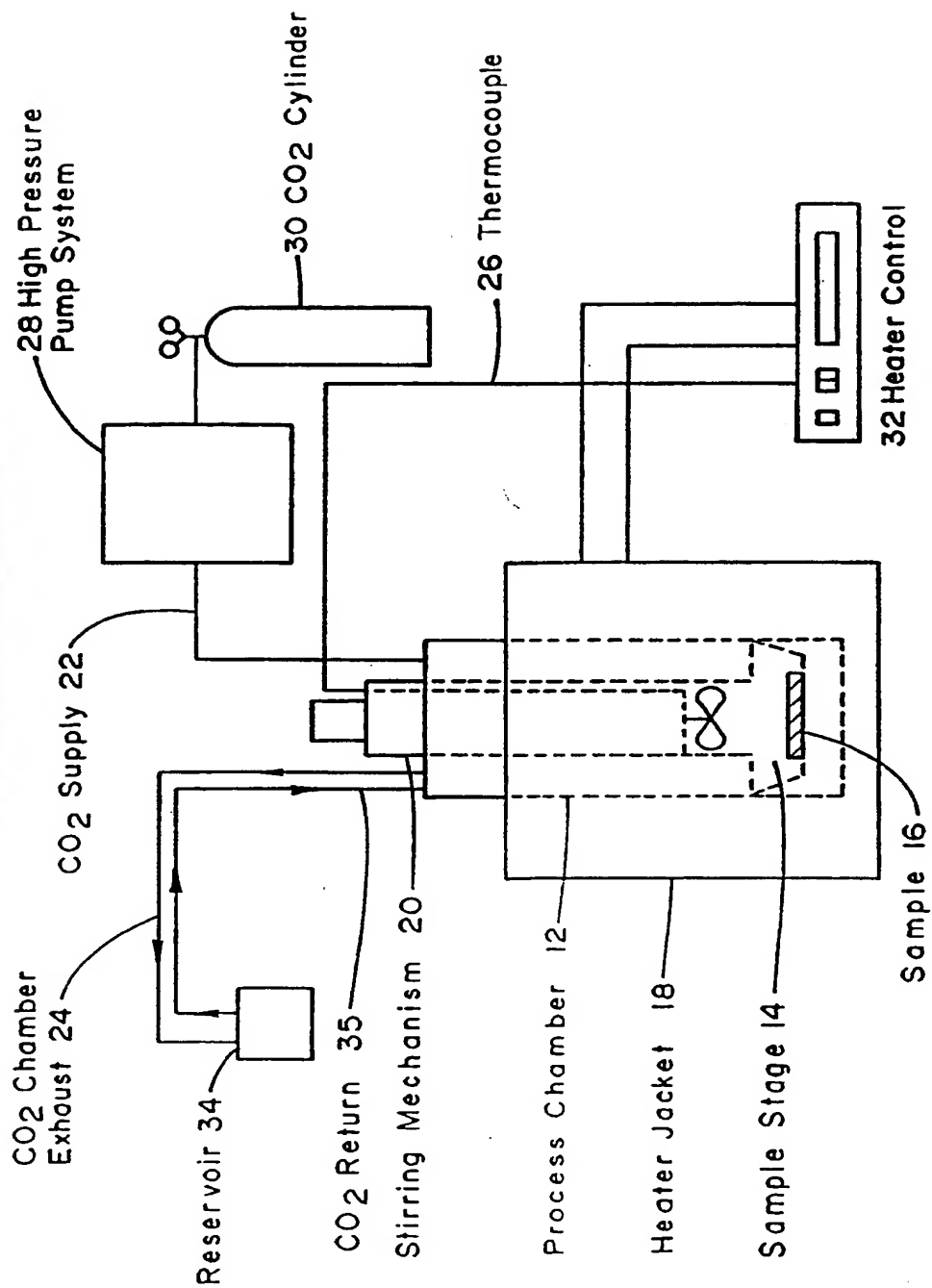


FIG.1





FIG.2

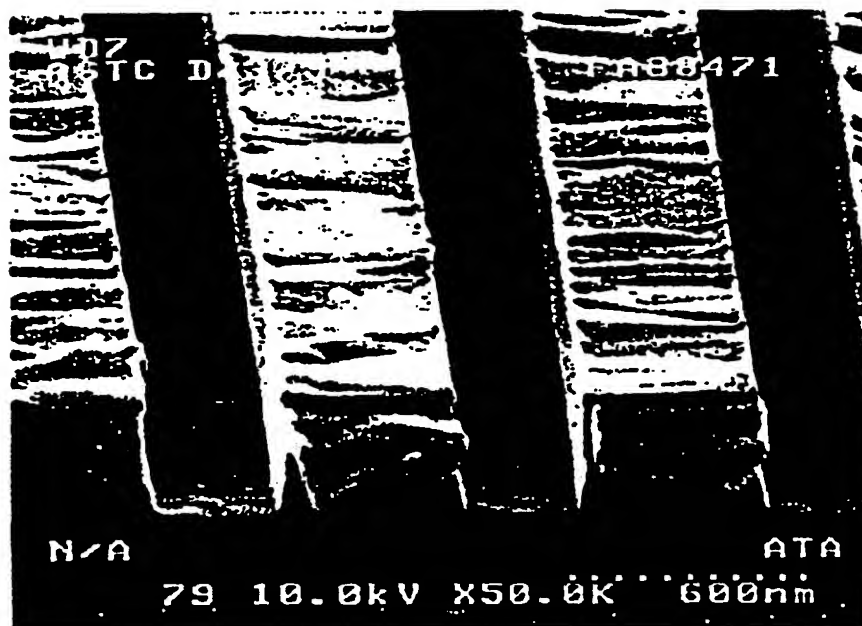


FIG.3

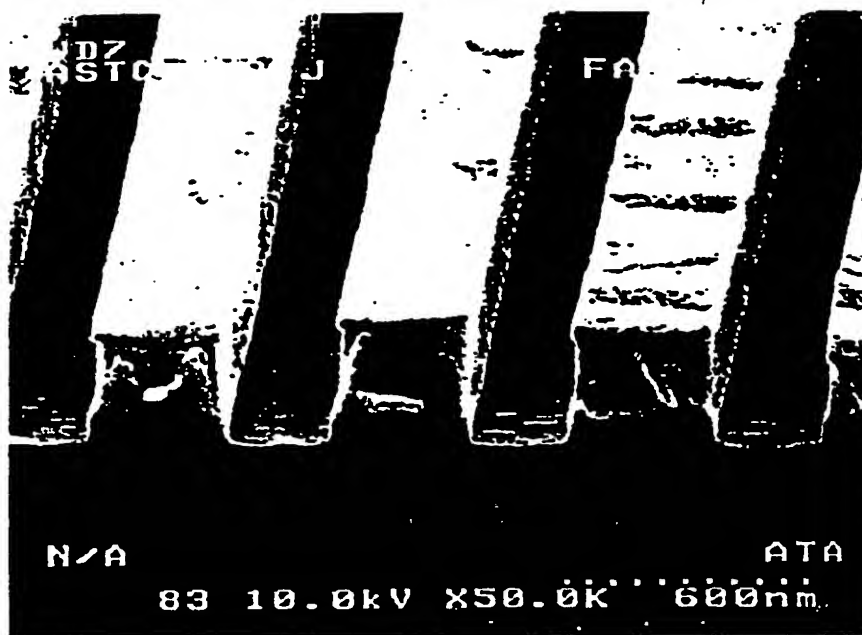


FIG.4



FIG. 5



FIG.6



FIG.7



FIG. 8

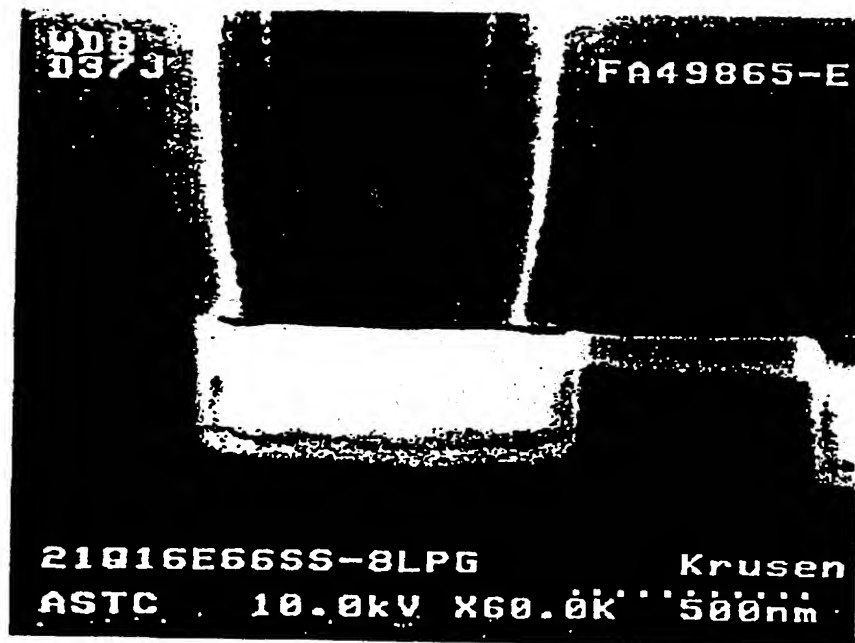
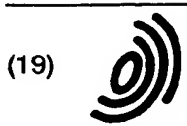


FIG.9





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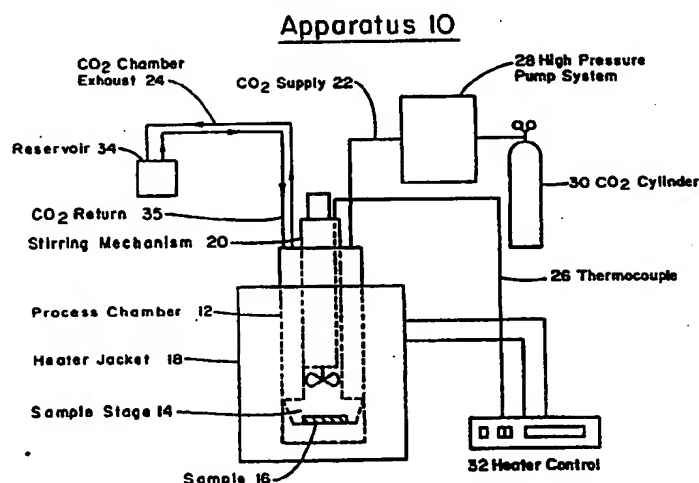


FIG. 1

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European Patent  
Office

## EUROPEAN SEARCH REPORT

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DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
X	EP 0 391 035 A (HUGHES AIRCRAFT COMPANY) 10 October 1990  * abstract * * column 1, line 7 - line 15 * * column 3, line 16 - line 26 * * column 5, line 52 - column 6, line 19 * * column 6, line 34 - line 36 * * column 9, line 47 - column 10, line 8 * * column 10, line 43 - column 11, line 8 * * column 11, line 46 - column 12, line 13; figures *	1-3, 7-10,15, 16,22,23	B08B7/00 H01L21/00
Y		4-6,11, 13,14, 19,20 12	
A			
Y	US 5 377 705 A (SMITH, JR. ET AL) 3 January 1995  * column 3, line 61 - column 4, line 8 * * column 4, line 19 - line 21 * * column 4, line 34 - line 48 * * column 5, line 33 - column 6, line 36; figures *	4-6,13, 14	TECHNICAL FIELDS SEARCHED (Int.Cl.6)  B08B
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Y	EP 0 587 168 A (LINDE AG) 16 March 1994 * abstract * * column 2, line 54 - column 3, line 36 * * column 5, line 1 - line 8 *	11,19,20	
A		1-7,12, 16	
The present search report has been drawn up for all claims			
Place of search <b>THE HAGUE</b>		Date of completion of the search <b>27 July 1998</b>	Examiner <b>Van der Zee, W</b>
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	

EPO FORM 1503 03 82 (P04C01)



European Patent  
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# EUROPEAN SEARCH REPORT

Application Number  
EP 97 30 7272

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
X	US 5 522 938 A (O'BRIEN) 4 June 1996  * column 1, line 12 - line 24 * * column 3, line 51 - line 60; figures *	1-4, 8, 15, 16, 23	
A	-----	5, 6	
A	US 5 267 455 A (DEWEES ET AL) 7 December 1993  * column 2, line 58 - line 68 * * column 3, line 19 - column 4, line 19 * * column 6, line 8 - line 24; figures * -----	1-6, 8, 13-16, 22	
The present search report has been drawn up for all claims			TECHNICAL FIELDS SEARCHED (Int.Cl.6)
Place of search <b>THE HAGUE</b>		Date of completion of the search <b>27 July 1998</b>	Examiner <b>Van der Zee, W</b>
<p><b>CATEGORY OF CITED DOCUMENTS</b></p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons &amp; : member of the same patent family, corresponding document</p>			

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